### **EE2013**

#### **NON-LINEAR CIRCUIT ANALYSIS**

#### **LECTURE 18: ACTIVE OPAMP FILTER DESIGN**

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# **LECTURE SCHEDULE**

Thursdays 11am-1pm (with short break)

Monday 9am-10am slot not used!

#### **LECTURE NOTES**

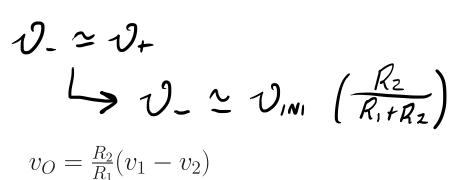
https://www.jaeger.ie/ee2013/lec17 Uploaded before lecture takes place

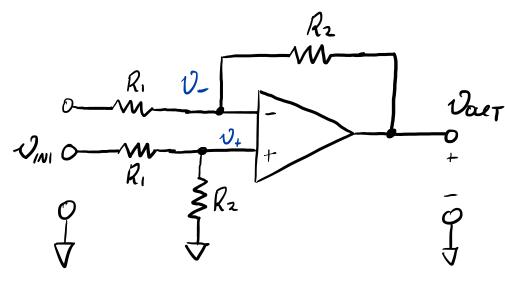
## **QUESTIONS?**

# Just ask whenever it comes to you! OR:

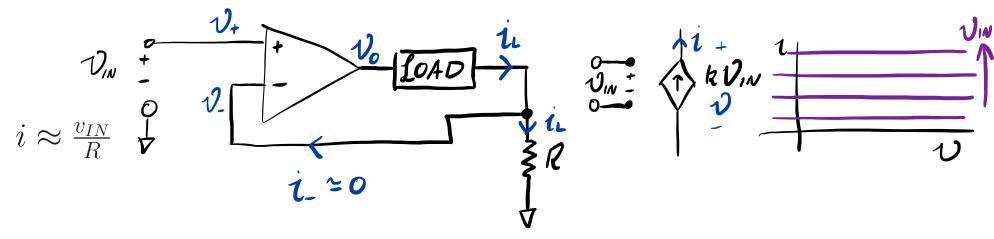
anthony.wall@mcci.ie on Email, Teams or Canvas

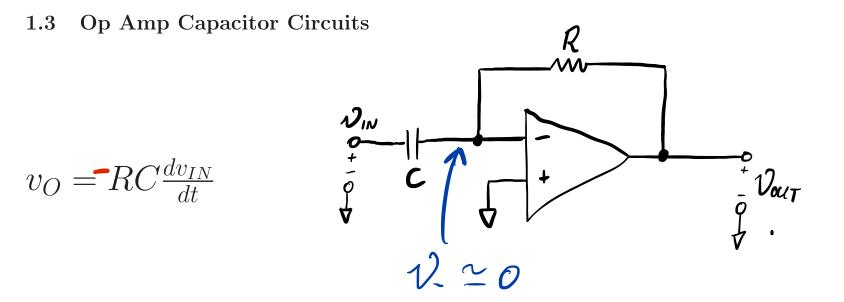
- 1 Review from Last Time
- 1.1 The Differential Amplifier

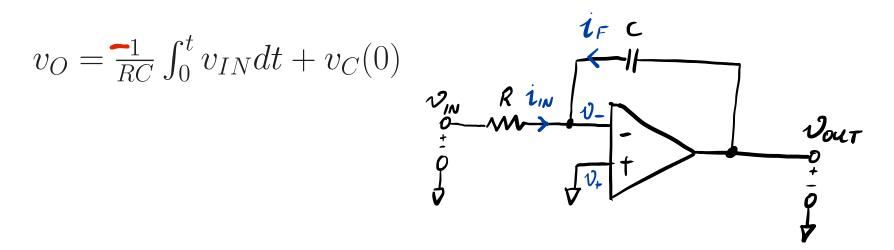




1.2 The Op Amp Current Source

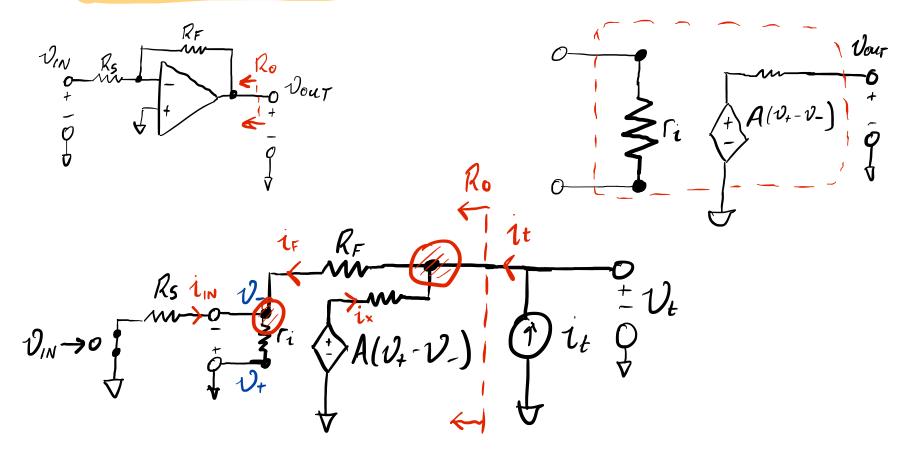


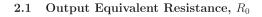


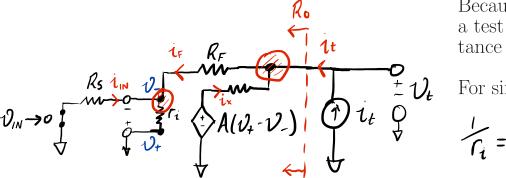


#### 2 Inverting Op Amp Resistance

Consider the inverting op amp circuit below. Ideally the output resistance (*i.e.*, the Thevenin equivalent resistance seen at the output terminals of the circuit) should be small to enable the circuit to drive subsequent stages. To calculate its value, we need to improve our op amp model by adding a (large) input resistance,  $r_i$ , and a (small) output resistance,  $r_o$ .







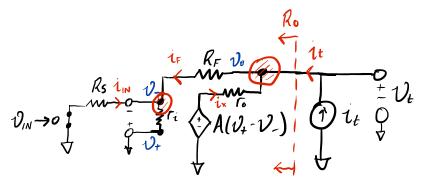
Because of the dependent source in the model, we need to apply a test source at the output terminal to calculate the output resistance of the circuit where  $R_O = \frac{v_o}{i_t}$ .

For simplicity we will employ conductances instead of resistances:

$$f_{i} = g_{i}$$
,  $f_{o} = g_{o}$ ,  $f_{R_{F}} = G_{F}$ ,  $f_{R_{S}} = G_{S}$ 

Clearly,  $v^+ = 0 \implies \mathcal{V}_- \cong \mathcal{O}$ 

Applying KCL at node  $v^-: \sum \dot{i}_{iN} - \sum \dot{i}_{out} = 0$ 



$$(O - V_{-}) \underbrace{6_{S}}_{S} + (\underbrace{V_{0}}_{O} - \underbrace{V}_{-}) \underbrace{6_{F}}_{G_{F}} - (\underbrace{V}_{-}) \underbrace{q_{i}}_{I} = 0$$

$$v^{-} = \frac{G_{F}v_{o}}{G_{S} + G_{F}} + \underbrace{q_{0}}_{G_{S}} + \underbrace{G_{F}v_{o}}_{G_{S} + G_{F}} \dots \text{for } \underbrace{g_{i} \to 0}_{G_{S} + G_{F}}$$
Applying KCL at node  $v_{o}$ :  $\sum i_{ov} - \sum i_{our} = 0$ 

$$i_{t} + \left[A(V_{t} - V_{-}) - V_{0}\right] \underbrace{q_{o}}_{O} - (V_{0} - V_{-}) \underbrace{6_{F}}_{F} = 0$$
Substituting for  $v^{+}$  and  $v^{-}$ :
$$-Ag_{o} \frac{G_{F}v_{o}}{G_{S} + G_{F}} - v_{o}g_{o} + i_{t} - v_{o}G_{F} + \frac{G_{F}^{2}v_{o}}{G_{S} + G_{F}} = 0$$
Simplifying:
$$-Ag_{o} \frac{G_{F}v_{o}}{G_{S} + G_{F}} - v_{o}g_{o} + i_{t} + v_{o}\left(\frac{-G_{S}G_{F} - G_{F}^{2} + G_{F}^{2}}{G_{S} + G_{F}}\right) = 0$$

$$\int_{O} e^{-\frac{i_{t}}{V_{0}}} = \frac{A \underbrace{q_{o}} \underbrace{6_{F}}_{G_{S} + G_{F}}}{\underbrace{6_{s} + 6_{F}}_{Ov}} \underbrace{f_{o}}_{Ov} \underbrace{f_{o}}_{V_{o}} + \underbrace{f_{o}}_{Ov} \underbrace{f_{o}}_{V_{o}} \underbrace{f_{o}} \underbrace{f_{o}}_{V_{o}} \underbrace{f_{o$$

Sain

Output

$$G_{O} \approx g_{o} \left(\frac{AG_{F}}{G_{S}+G_{F}}+1\right) = g_{o} \left(1+\frac{AR_{S}}{R_{S}+R_{F}}\right)$$

$$Output$$

$$Internal$$

$$Internal$$

$$Internal$$

$$Internal$$

$$Ioop$$

$$Ioop$$

$$Ioop$$

$$Ioop$$

This, in fact, is a general result for any linear circuit in which the feedback resistor is sampling the output node voltage (rather than the output current).

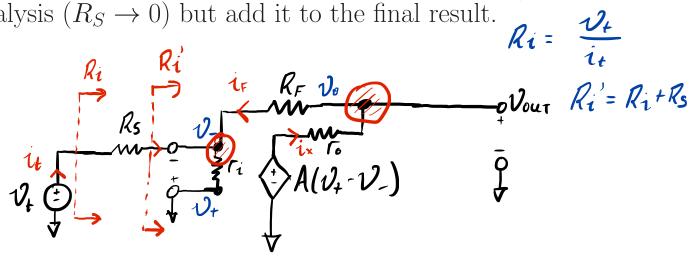
Where A dominates the loop gain such at  $A \gg 1 + \frac{R_F}{R_S}$ , then:

$$R_o \approx \frac{r_o}{A\frac{R_S}{R_S + R_F}} \approx \frac{r_o(R_S + R_F)}{AR_S}$$

Typically,  $r_o \approx 1000 \ \Omega$  and  $R_o \lesssim 0.1 \ \Omega$ .

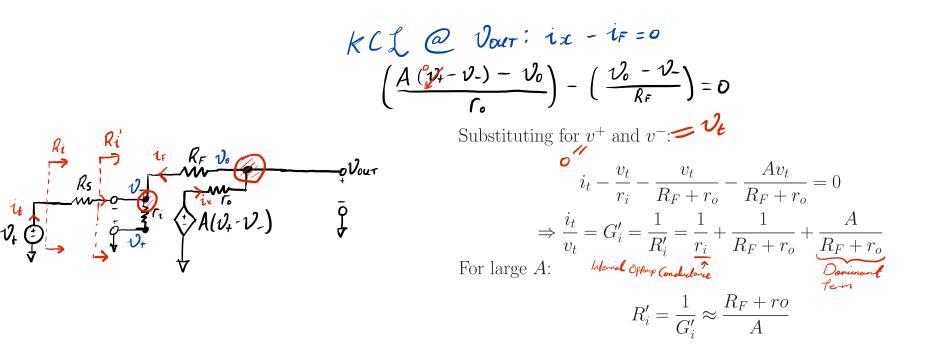
#### **2.2** Input Equivalent Resistance, $R_i$

To calculate the input equivalent resistance, we add a test voltage source at the input. The analysis is simplified if we ignore  $R_S$  in the analysis  $(R_S \rightarrow 0)$  but add it to the final result.



Clearly:  $v^+ = 0$ 

$$v^{-} = v_{t} \text{ for } R_{S} \to 0$$
  
Applying KCL at node  $v^{-}: \sum_{i} i_{iN} - \sum_{iout} = 0$   
 $i_{t} + \frac{v_{o} - v_{-}}{R_{F}} - \frac{v_{-}}{r_{i}} = 0$  For  $R_{i}$ , Let  $v_{-} = v_{t}$ 



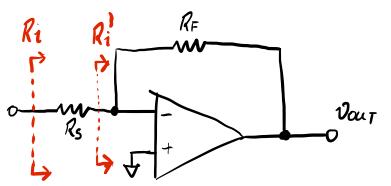
For large  $A \sim 10^5$ , the input terminal resistance will be very small  $(R_i \leq 1\Omega)$ . This is intuitive because even for small input voltages  $(v_{IN} \leq 1 \text{ mV})$ , the output voltage,  $v_O$ , is -A times larger. So this large voltage has to be dropped entirely across the feedback resistor,  $R_F$ , which carries relatively large currents even when the input voltage is small. This large feedback current for small input voltage indicates that the effective input resistance is very small.

The complete input resistance of the circuit includes the effect of  $R_S$ :

$$R_i = R_S + R'_i = R_S + \frac{R_F + r_o}{A}$$

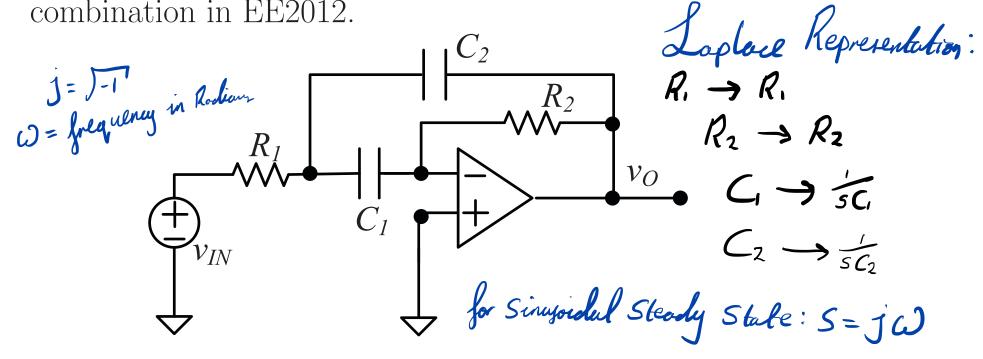
Since  $R'_i$  is small, it is  $R_S$  which usually dominates the input resistance:

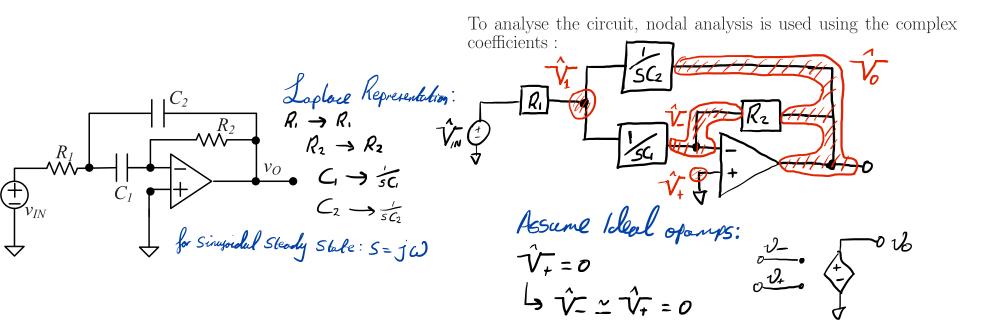




#### 3 Active Op Amp Filters

Inductors are not ideal elements for integrated circuits because they are difficult to fabricate on silicon and can introduce system variability. Therefore, very large scale integrated (VLSI) circuit designers will seek to eliminate inductors with *active* elements such as transistors and op amps for low power applications. The following example illustrates this in the case of an *active* bandpass filter which we previously saw implemented as a passive *RLC* combination in EE2012.

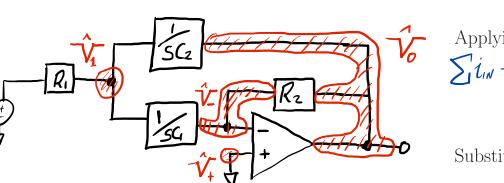




Applying KCL at node  $\hat{V}_1 : \sum \hat{\mathcal{I}}_{IN} - \sum \hat{\mathcal{I}}_{OUT} = \mathcal{O}$ 

 $(\hat{V}_{W} - \hat{V}_{I})G_{I} + (\hat{V}_{o} - \hat{V}_{I})sG_{2} - \hat{V}_{I}sG_{I} = 0$ 

$$G_1 = \overrightarrow{R}_1 \quad G_2 = \overrightarrow{R}_2$$



where 
$$G_1 = 1/R_1$$
 and  $G_2 = 1/R_1$   
Applying KCL at node  $\hat{V}^-$  (where  $\hat{V}^- = 0$ ):  
 $\int \dot{i}_{iN} - \int i \delta u_r = \hat{V}_1 s \hat{C}_1 + \hat{V}_2 \hat{C}_2 = 0$   
 $\hat{V}_1(sC_1) + \hat{V}_0 \hat{C}_2 = 0 \implies \hat{V}_1 = -\frac{\hat{V}_0 \hat{C}_2}{\hat{S}C_1}$ 

Substituting for  $\hat{V}_1$  in the expression for above:

$$\hat{V}_{IN}G_1 + \frac{\hat{V}_OG_1G_2}{sC_1} + \hat{V}_OsC_2 + \frac{\hat{V}_OG_2sC_2}{sC_1} + \frac{\hat{V}_OG_2sC_1}{sC_1} = 0$$

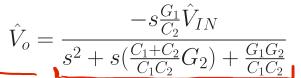
$$\hat{V}_O = \frac{-G_1 \hat{V}_{,N}}{\frac{G_1G_2}{SG_1} + SG_2 + \frac{G_2G_2}{G_1} + G_2}$$

Multiplying by  $s/C_2$ :

 $\hat{V}_{o} = \frac{-\frac{G_{1}s}{C_{2}}\hat{V}_{IN}}{s^{2} + s(\frac{G_{2}}{C_{1}} + \frac{G_{2}}{C_{2}}) + \frac{G_{1}G_{2}}{C_{1}C_{2}}}$   $\hat{V}_{o} = \frac{\hat{V}_{o}}{s^{2} + s(\frac{G_{1}+C_{2}}{C_{1}C_{2}}G_{2}) + \frac{G_{1}G_{2}}{C_{1}C_{2}}} = \mathbf{0}$ 

 $.5^{2}+2a_{5}+\omega_{0}^{2}=$ 

 $V_{o}$ 



Comparing the expression here to that from the passive bandpass filter in EE2012 formed by a series RLC combination:

Clearly, the two circuits have transfer functions with very similar form except for the -s term in the numerator of the active filter (which introduces a 180° phase shift in the output relative to the input). Othewise, the component values can be chosen to have very similar properties to the passive filter:

	Passive BP Filter	Active BP Filter
Characteristic equation	$s^2 + s\frac{R}{L} + \frac{1}{LC}$	$s^{2} + s \frac{C_{1} + C_{2}}{C_{1}C_{2}}G_{2} + \frac{G_{1}G_{2}}{C_{1}C_{2}}$
Natural frequency $\omega_0$	$\sqrt{\frac{1}{LC}}$	$\sqrt{rac{G_1G_2}{C_1C_2}}$
Damping factor $\alpha$	$\frac{R}{2L}$	$\tfrac{C_1+C_2}{2C_1C_2}G_2$

A large number of passive filter configurations can be implemented as active filters, thus simplifying VLSI circuit design variabilities due to inductors. The caveat is that active filters are typically only suitable in low-power on-chip applications.