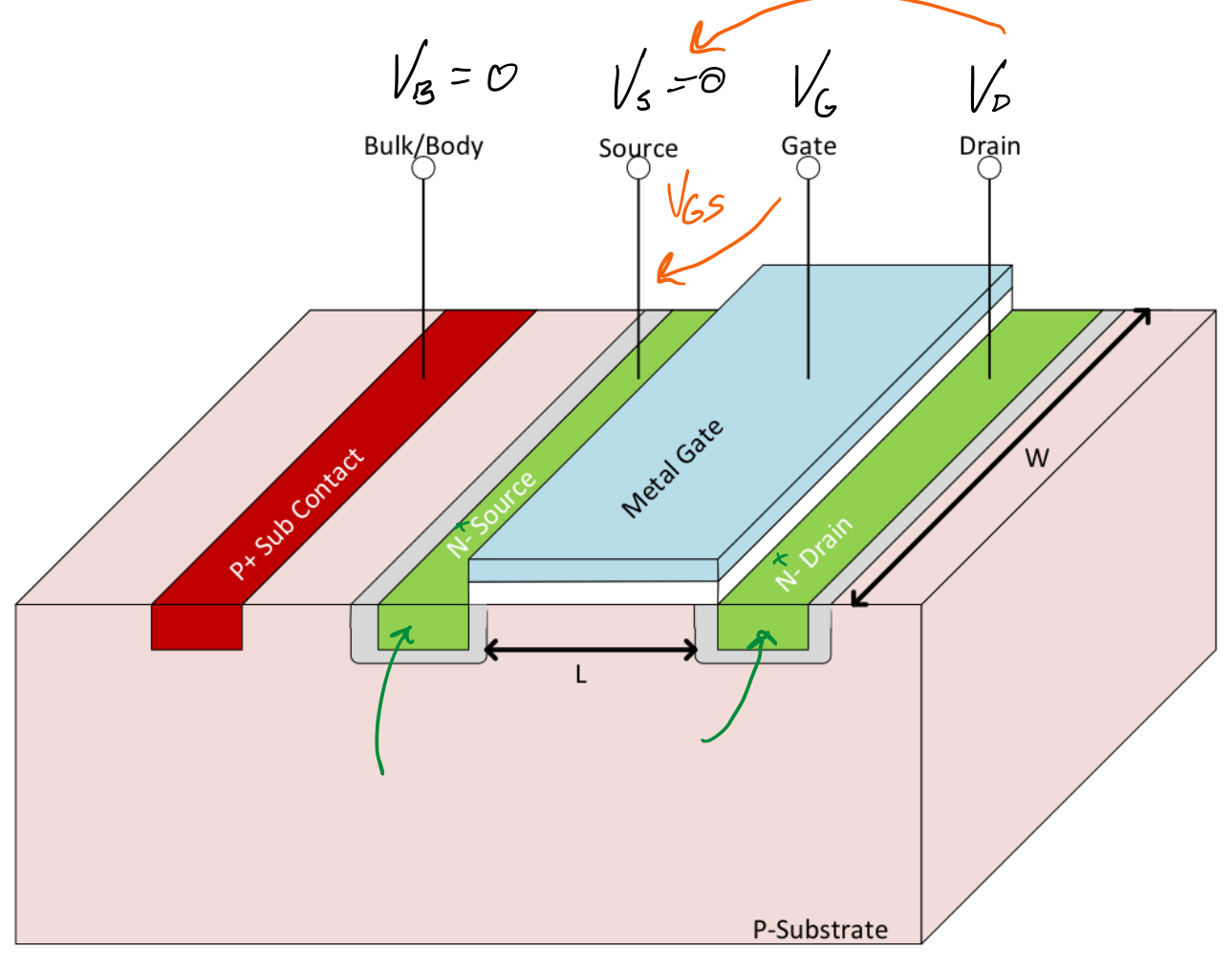


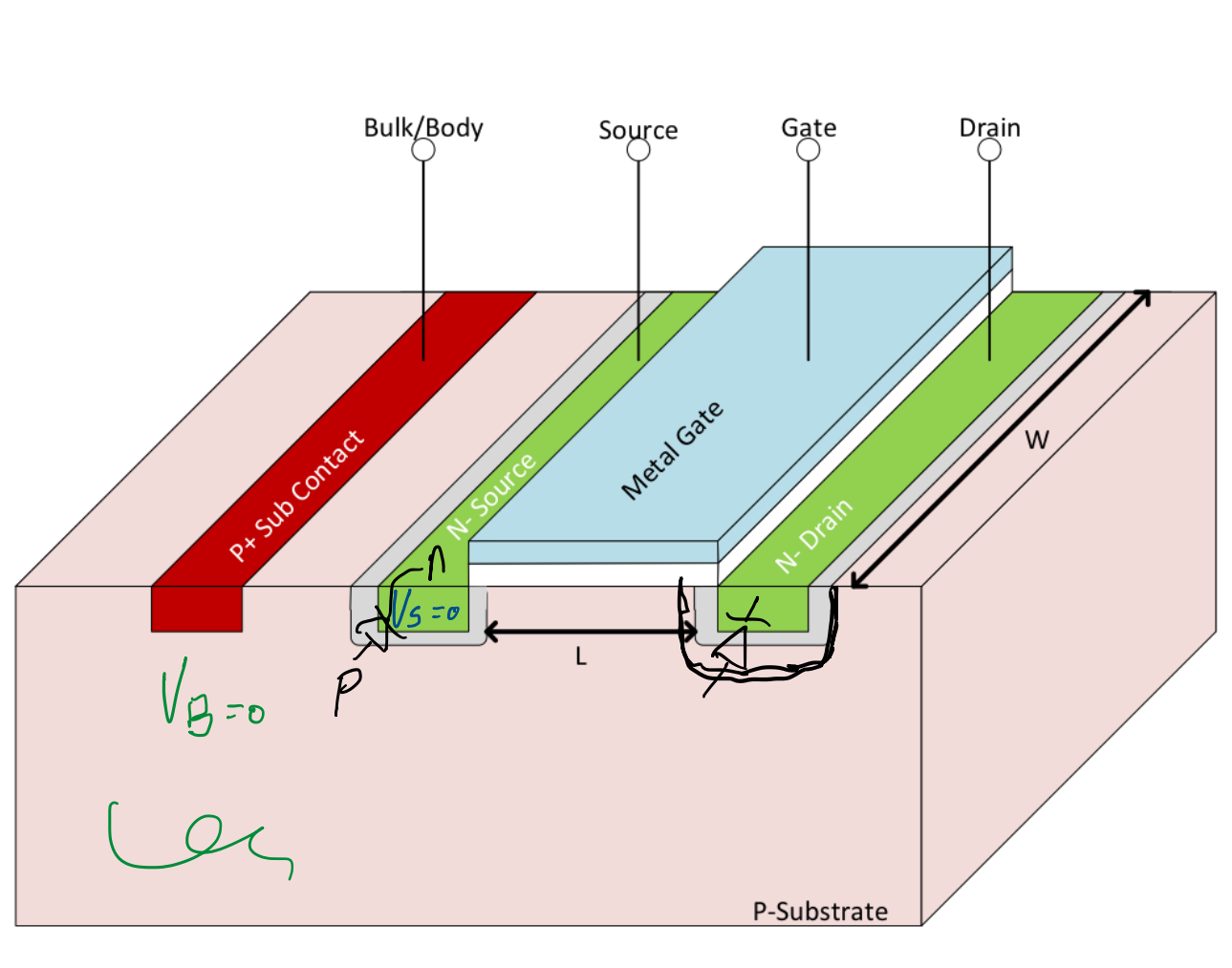
# MOSFETS Part 10: Adding Source & Drain - Making Current Flow!

- MOSFET Formed by placing n+ Source and Drain Regions on either side of the gate Diffusions
- Note Bulk/Body contact on the top for ease of manufacture
  - No Different to previous analysis
- Contacts are typically assumed to be biased:
  - Bulk:  $V_B = 0$
  - Source:  $V_S = 0$
  - Gate:  $V_G = \text{Variable} = V_{GS}$
  - Drain:  $V_D = \text{Variable} = V_{DS}$



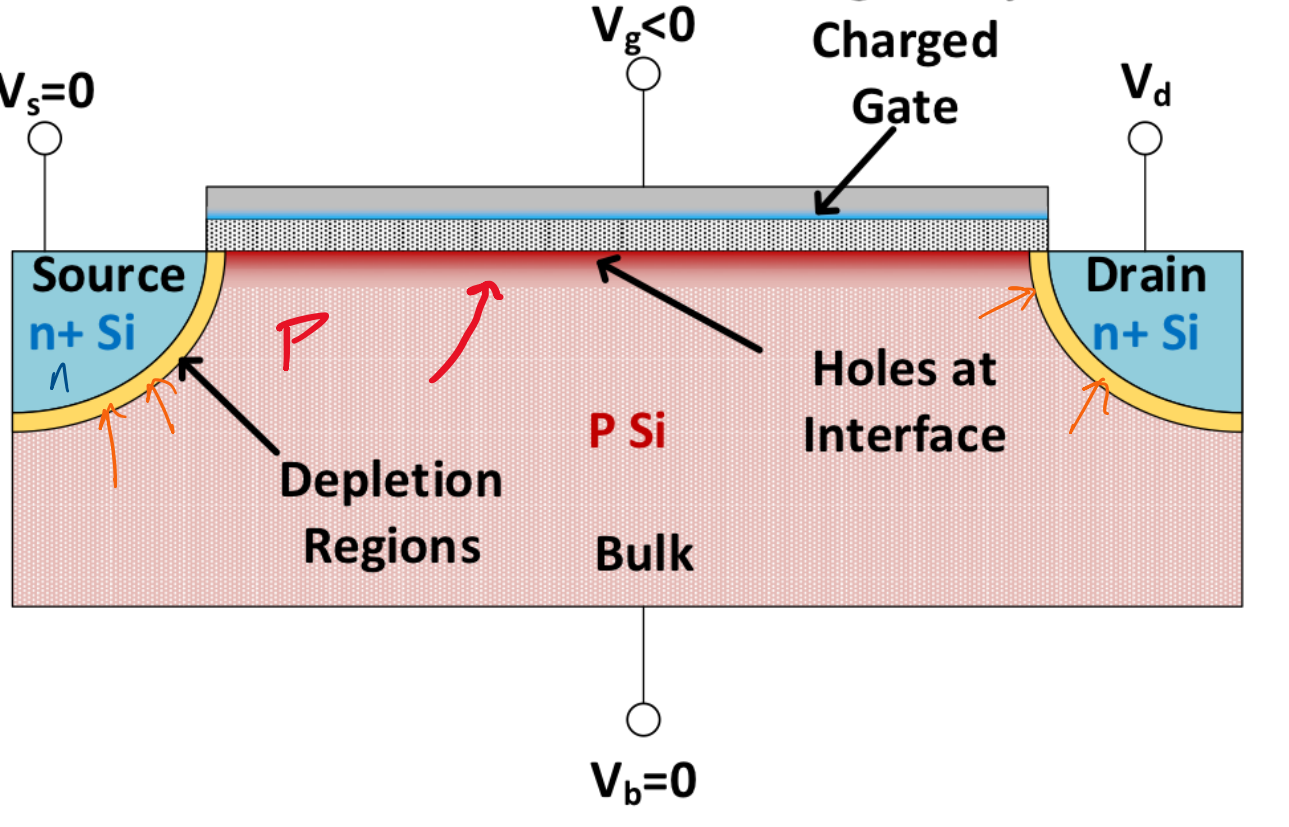
## Zero Gate Voltage

- Let:  $V_G = 0$  (or Conversely  $V_{GS} = 0$ )
- Bulk is p-type and Source is n+ type
  - $V_B = 0$
  - Depletion Region around source from unbiased P-N Junction
- Bulk is p-type and Drain is n+ type
  - $V_B = 0$ ,  $V_D > 0$
  - Large Depletion region around Drain from reverse Biased P-N Junction
- No Current can flow!
- Channel is either in accumulation or depletion



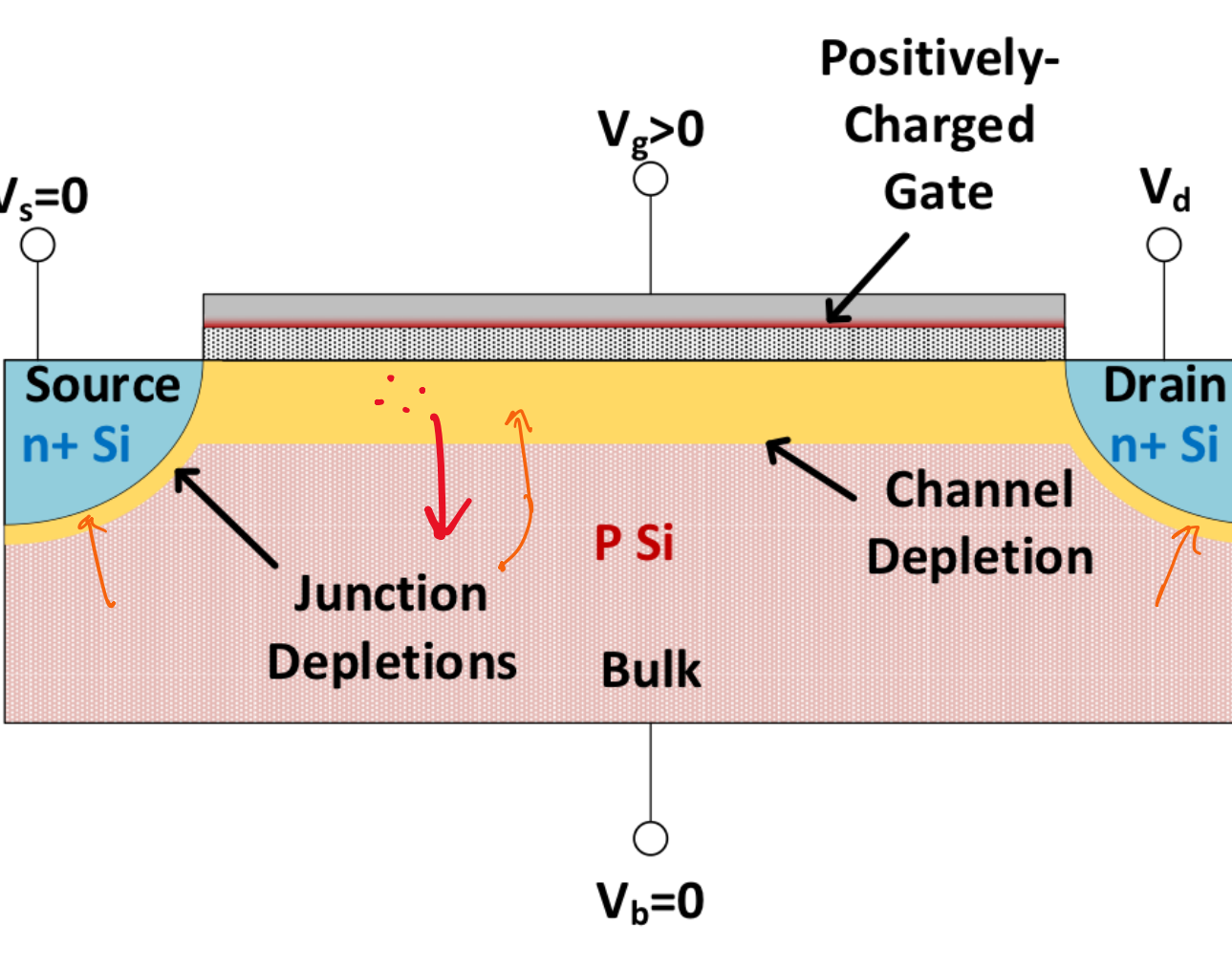
## Negative Gate Voltage

- Let:  $V_G < 0$
- Channel Region enters Accumulation
- Majority Carriers accumulate at the interface
- Depletion Region remains around Drain & Source
- No Current can Flow!



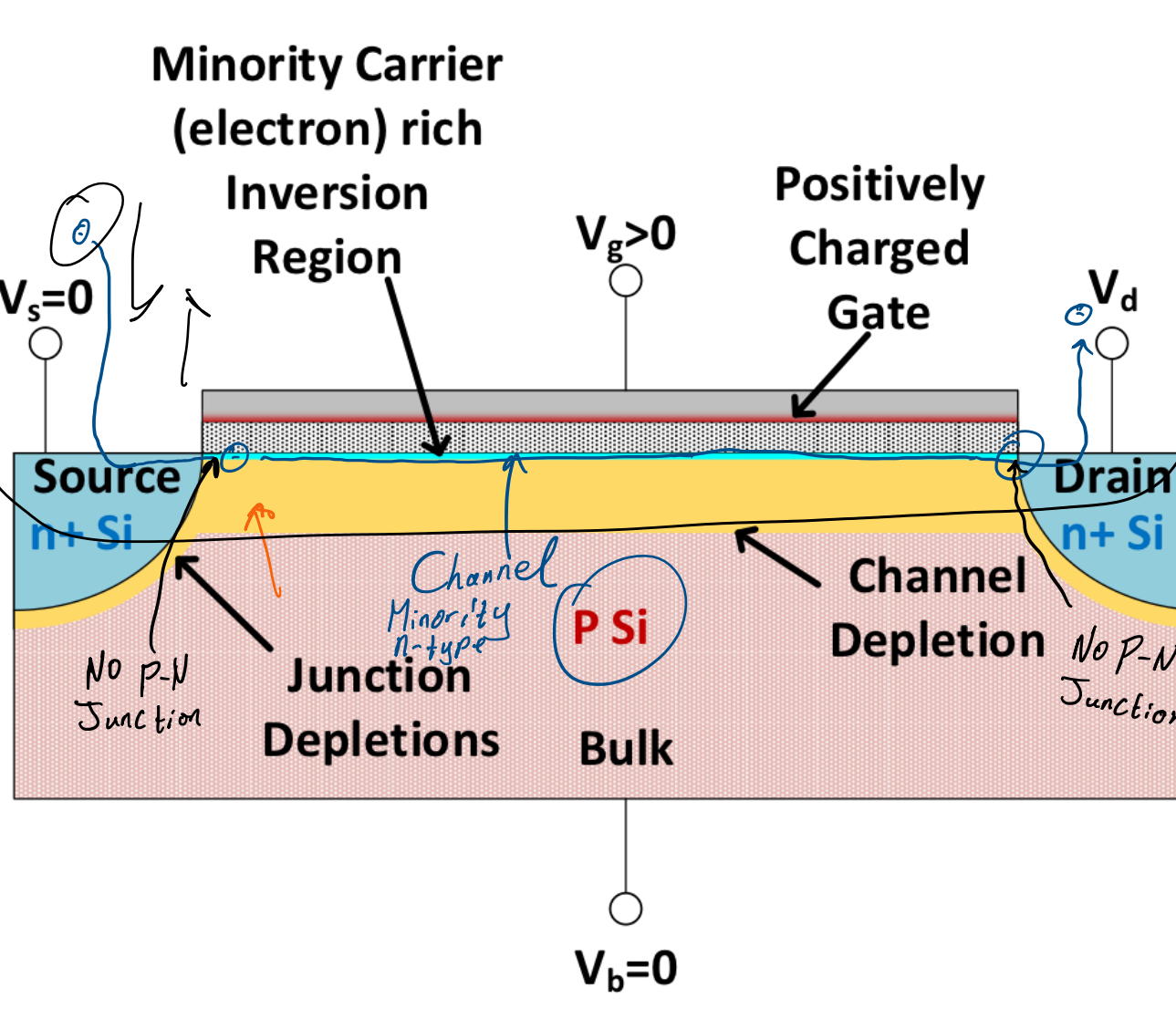
## Small Positive Gate Voltage

- Let:  $V_G > 0$ , Small
- Majority Carriers pushed out of interface
- Interface enters Depletion
- Depletion region continuous between source & drain
- No current Can flow



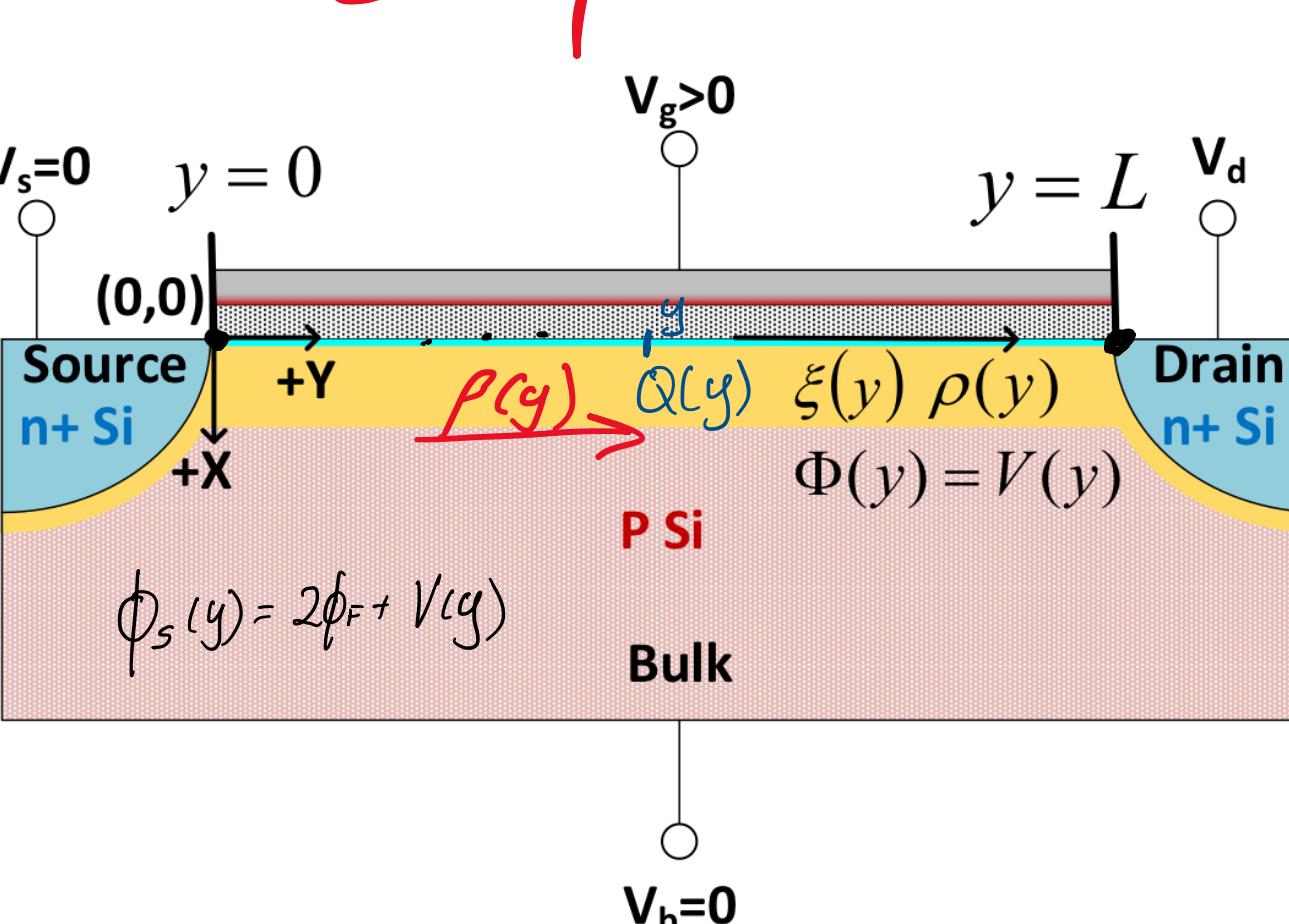
## Large Positive Gate Bias = Inversion

- Let:  $V_G \gg 0$
- Interface enters Strong Inversion
- Minority Carriers form Channel
- Channel is effectively n-type
  - No P-N Junction between Source & Channel
  - No P-N Junction between Drain & Channel
- Current can flow between Source and Drain!



## Current in the MOSFET: Setup

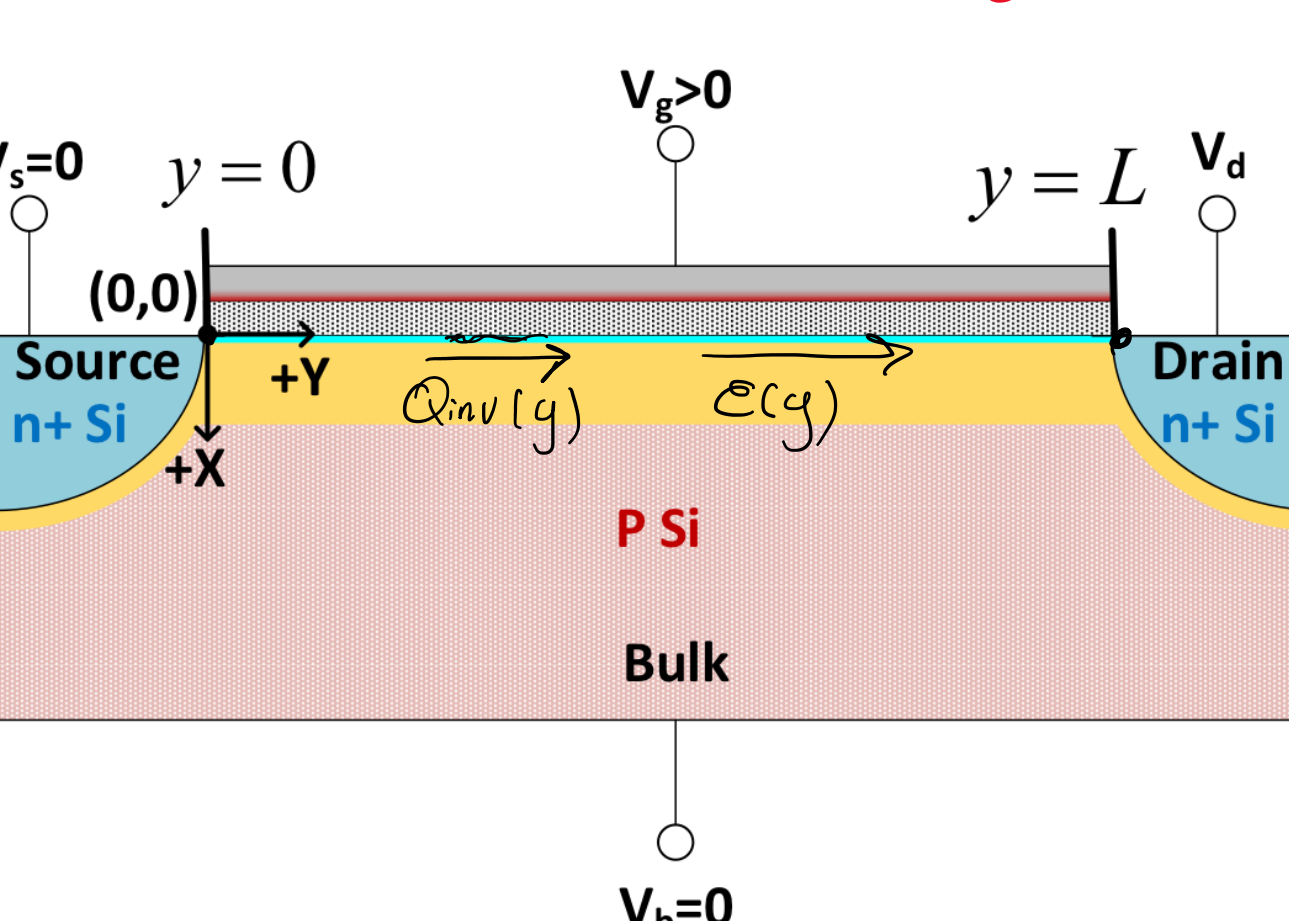
- Place a Voltage on the Drain  $V_D$ 
  - Now a Potential difference between Drain and Source  $V_{DS}$
- As a result, a Current will flow between Drain & Source
- We define the horizontal as the y axis with the following quantities:
  - $V(y)$  ← Horizontal Component of the Channel Voltage
  - $\phi_s(y)$  ← Surface Potential on a function of y
  - $E(y)$  ← E-field
  - $\rho(y)$  ← Chg density
  - $Q(y)$  ← total charge in a given slice @ y
- We define the Drain and Source Voltages:
  - $V(y=0) = 0$
  - $V(y=L) = V_D$
- Remember that Electric Field is just the rate of change of potential:
  - $E(y) = -\frac{dV(y)}{dy}$
- Uniform Carrier Concentration in the channel → Current is dominated by Drift Current:
  - Current Density  $J = \sigma E$  (E-field, conductivity)
  - But:  $\sigma = q\mu n$
- Conductivity depends on charge in the channel!
  - This is why we went to so much effort to find channel charge!



So:  $J = Q\mu_n E(y)$

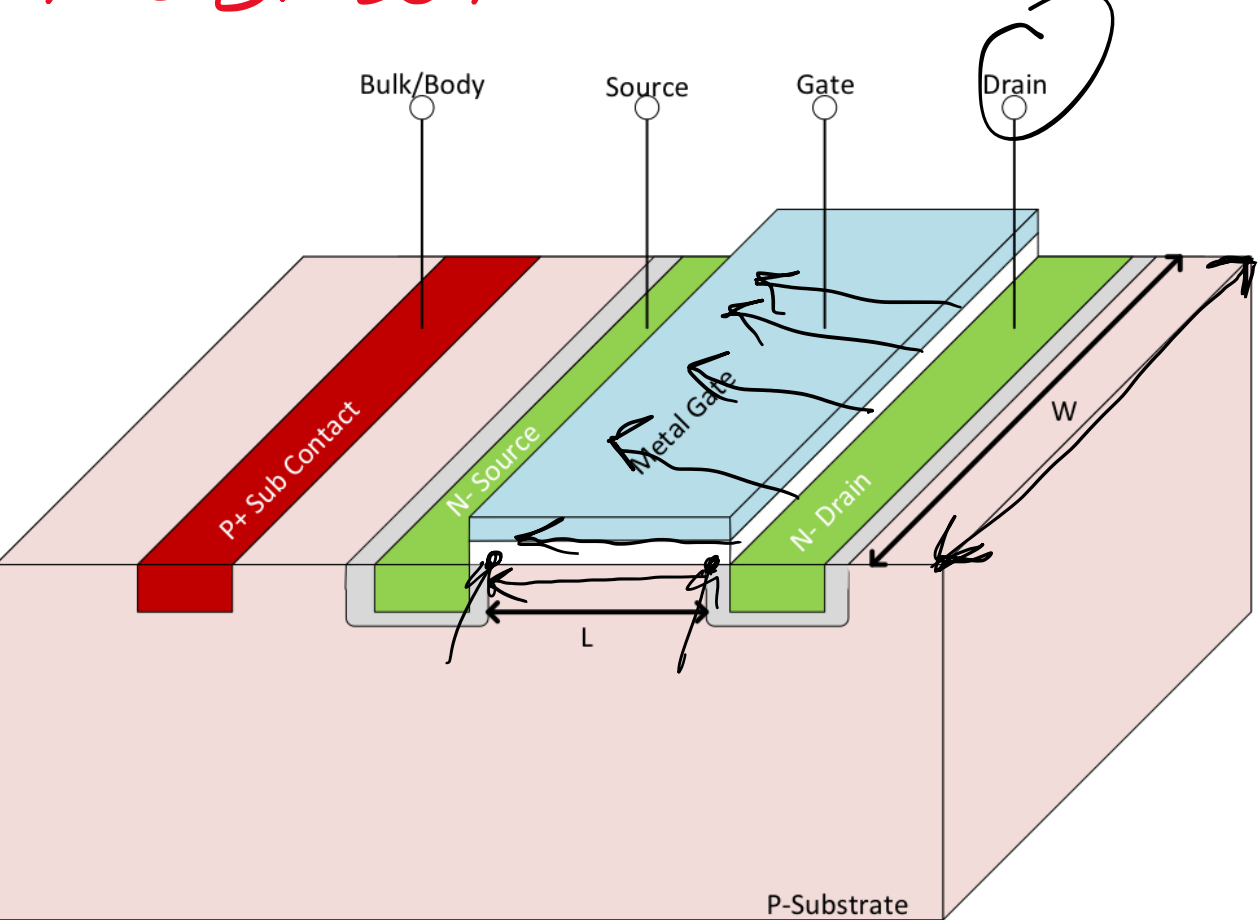
## Current in the MOSFET: Current Density

- In the Context of the MOSFET, The Current Density is given by:
  - $J(y) = Q_{inv}(y)\mu_n E(y)$
- The Inversion Charge  $Q_{inv}$  may not be constant along the channel due to Drain Voltages (shown later)
- The Electric Field  $E(y)$  is due to Drain-Source Voltage  $V_{DS}$
- Once we know Current Density, We can find total current in the MOSFET...



## Current in the MOSFET

- Current Density is Current per unit Width:
  - $J = \frac{I}{W}$
  - $I = -J \times W$
- So the total Drain Current is given by:
  - $I_D = -W \int_0^L J(y) dy$
  - $I_D = -W \mu_n Q_{inv}(y) E(y)$
  - $I_D = -W \mu_n Q_{inv}(y) \frac{dV(y)}{dy}$
- But what is  $Q_{inv}(y)$ ?



## Inversion Charge Along y

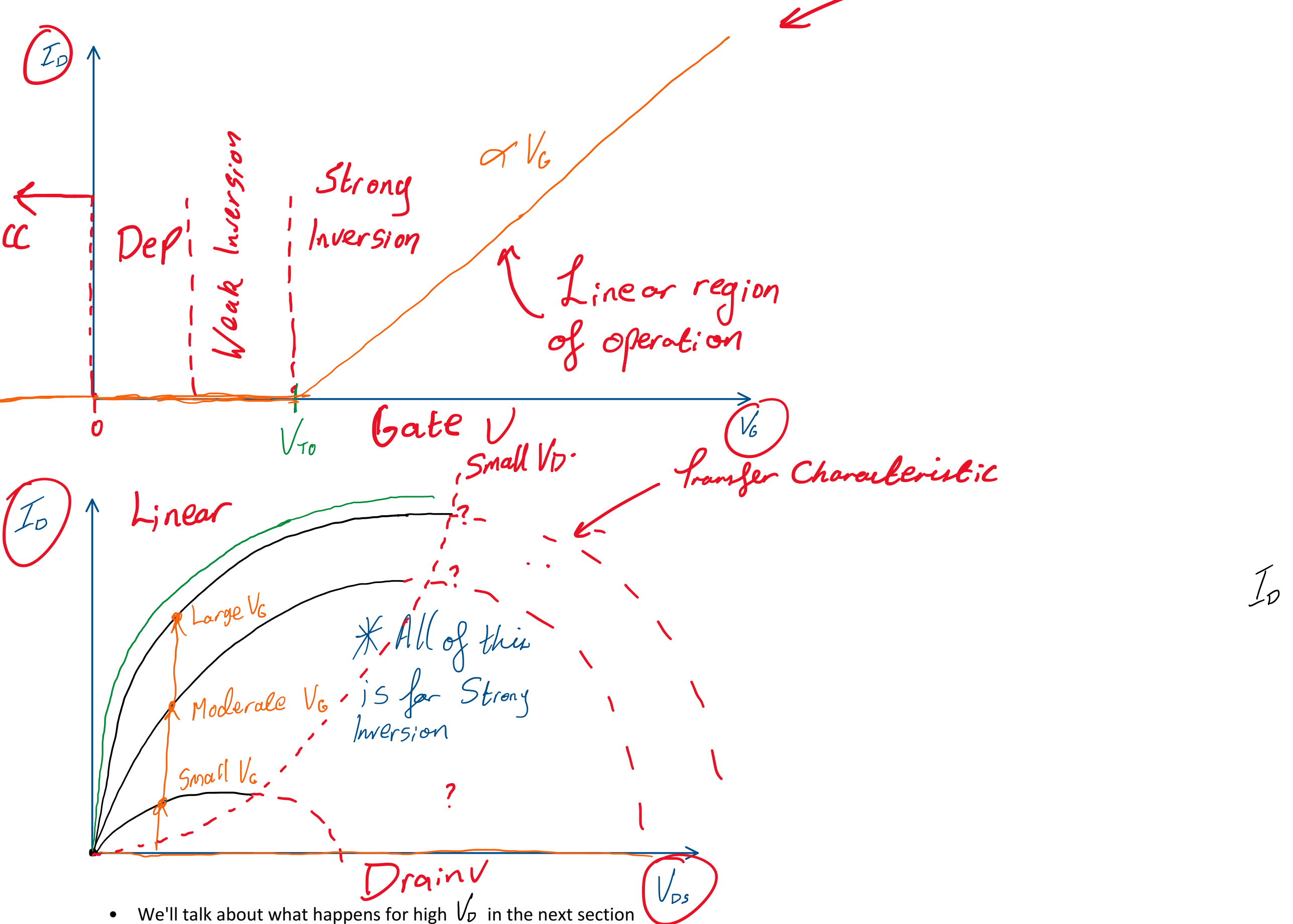
- We know total charge at the gate:
  - $Q = -(Q_{dep}(y) + Q_{inv}(y)) \Rightarrow Q_{inv} = -Q_G - Q_{dep}$
- Depletion Charge is given by:
  - $Q_{dep} = -\sqrt{2q\epsilon_s N_A \phi_s(y)}$
- Charge Equation gives us total Gate Charge:
  - $Q_G = C_{ox} [V_G - \phi_s(y)]$
- Substituting these to find  $Q_{inv}$ :
  - $Q_{inv}(y) = -(C_{ox} [V_G - \phi_s(y)] - \sqrt{2q\epsilon_s N_A \phi_s(y)})$
- We should now be able to solve for Drain Current:
  - $I_D = -W \mu_n Q_{inv}(y) \frac{dV(y)}{dy}$
  - $I_D dy = -W \mu_n Q_{inv}(y) dV(y)$
- In order to remove the derivative (d) terms, we must integrate both sides:
  - $\int_0^L I_D dy = \int_{V_{GS}}^{V_D} -W \mu_n Q_{inv}(y) dV(y)$
  - This integration is very involved. The solution is given:
    - $I_D = \frac{W}{2} \mu_n C_{ox} \left( [V_G - 2\phi_F - \frac{V_{DS}}{2}] V_{DS} - \frac{2}{3} \sqrt{2q\epsilon_s N_A} [(2\phi_F + V_{DS})^{3/2} - (2\phi_F + V_G)^{3/2}] \right)$
  - This solution is pretty complicated, but can be approximated by:
    - $I_D \approx \frac{W}{2} \mu_n C_{ox} \left( \frac{1}{2} V_{GS}^2 + V_{GS} V_{DS} - 2\phi_F V_{DS} - \frac{2}{3} \sqrt{2q\epsilon_s N_A} [(2\phi_F + V_{DS})^{3/2} - (2\phi_F + V_G)^{3/2}] \right)$

Let  $\gamma = \frac{\sqrt{2q\epsilon_s N_A}}{C_{ox}}$

$I_D = \frac{W}{2} \mu_n C_{ox} \left( \frac{1}{2} V_{GS}^2 + V_{GS} V_{DS} - 2\phi_F V_{DS} - \frac{2}{3} \gamma \sqrt{2q\epsilon_s N_A} [(2\phi_F + V_{DS})^{3/2} - (2\phi_F + V_G)^{3/2}] \right)$

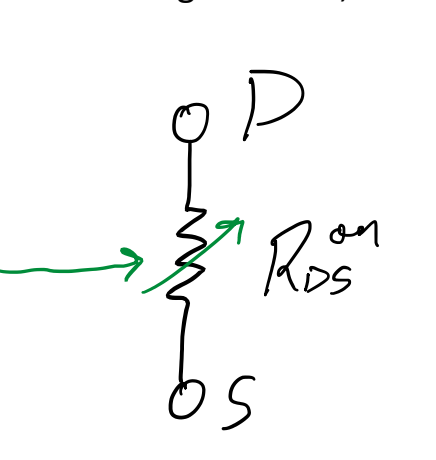
$V_{GS} = V_{GS} + 2\phi_F + \frac{\sqrt{2q\epsilon_s N_A} \phi_F}{C_{ox}}$

$= V_{GS} + 2\phi_F + \gamma \sqrt{2\phi_F}$



## Small Signal Model

- This is called the Linear Region since drain current is linear with  $V_G$
- The drain current is quadratic with respect to  $V_D$  however.
- We assume that the drain voltage is small, and model the device as a gate-controlled Resistor:



$\frac{1}{R_{DS(on)}} = \frac{dI_D}{dV_D} = \frac{W}{2} \mu_n C_{ox} [V_G - V_{TO}] = \frac{W}{2} \mu_n C_{ox} (V_G - V_{TO})$